

0803

Sheet 1 of 1

| | | | |
|---|--|-------------------------------------|--|
| FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office | | Attorney Docket Number 5649-1145 | Serial No. To Be Assigned <i>20/640 082</i> |
| LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary) | | Applicants: Chang-Hyun Lee | |
| | | Filing Date: Concurrently Herewith | Group <i>2827</i> |

U. S. PATENTS & PATENT APPLICATION PUBLICATIONS

| Examiner Initial <i>TNT</i> | | Document Number | Date | Name | Class | Subclass | Filing Date if Appropriate |
|--------------------------------|----|-----------------|----------|--------------------|-------|----------|----------------------------|
| | 1. | 5,677,873 | 10/14/97 | Choi et al; | 365 | 185.17 | |
| | 2. | 5,715,194 | 02/03/98 | Hu | 365 | 185.17 | |
| | 3. | 5,768,188 | 06/16/98 | Park et al; | 365 | 185.03 | |
| | 4. | 5,991,202 | 11/23/99 | Derhacobian et al; | 365 | 185.19 | |
| | 5. | 6,061,270 | 05/09/00 | Choi | 365 | 185.02 | |

FOREIGN PATENT DOCUMENTS

| | | Document Number | Date | Country | Class | Subclass | Translation Yes No |
|--|--|-----------------|------|---------|-------|----------|----------------------|
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|------------|----|--|
| <i>TNT</i> | 6. | Jae-Duk Lee; "Effects of Floating-Gate Interference on NAND Flash Memory Cell Operation", IEEE Electron Device Letters, Vol. 23, No. 5 (May 2002). |
| | | |
| | | |
| | | |

EXAMINER
*EXAMINER*TAN I. Nivison*

DATE CONSIDERED

03/14/05

Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.